

FY24 Strategic University Research Partnership (SURP) **Monolithic W-Band Frequency Synthesizer**

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Publication and Experimental Results Summary

Design/ Author		VCC (V)	Current	Phase Noise*	Max. Frequency	Tuning Range	FoM ₁ *	FoM _T *	No of Cores	Technology
This Work	Design 1	1.5 V	5.5 mA (max)	-96.5 dBc/Hz	37.3 GHz	3.2 GHz (9.0 %)	-179.2 dB	-179.2 dB	1	180 nm SiGe
	Design 2	1.5 V	22 mA (max)	-102.3 dBc/Hz	35.1 GHz	2.0 GHz (5.5 %)	-178.0 dB	-173.4 dB	4	180 nm SiGe
	Design 3	1.5 V	88 mA (max)	-106.9 dBc/Hz	34.7 GHz	3.1 GHz (8.7 %)	-176.5 dB	-175.9 dB	16	180 nm SiGe
	Design 4	1.5 V	352 mA (max)	Oscillators did not lock into a single frequency.					64	180 nm SiGe
	Design 5	1.5 V	1408 mA (max)	Oscillators did not lock into a single frequency.					256	180 nm SiGe

$$\operatorname{FoM}_{1} = PN \Big|_{dB} - 20 \log \left(\frac{f}{\Delta f}\right) + 10 \log \left(\frac{P_{DC}}{1mW}\right)$$

National Aeronautics and Space Administration

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$$\operatorname{FoM}_{T} = PN \Big|_{dB} - 20 \log \left(\left(\frac{f_{osc}}{\Delta f} \right) * \left(\frac{TR\%}{10} \right) \right) + 10 \log \left(\frac{P_{DC}}{1 \text{ mW}} \right)$$

* Phase noise, FoM, FoM_{τ} and are considered with 1 MHz offset from carrier frequency.

Significance/Benefits to JPL and NASA: The long-term goal of this collaboration is to develop a compact W-band transceiver for application in Wband radars in upcoming missions. The development of this compact radar has several significant building blocks that require innovations in mm-wave circuit design and development to produce a compact system. One of which is a very low phase noise oscillator that results in a high-performance LO for RF and a high-performance clock for digital subsystems of the radar. Hence resulting in high performance radar that allows optimization of velocity and range ambiguities for radar landers and precise measurements in minuscule fragments of molecules and particles of space and earth atmospheric environment.

Publications:

NA

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